ABSTRACT OF THE DISCLOSURE

A circuit for a power amplifier is disclosed which amplifies and outputs an audio signal by amplifying an input audio signal using first and second differential circuits, and driving a push-pull output transistor with the outputs from the first and second differential circuits. circuit includes a signal generating part generating a disconnection timing signal for disconnecting a bias current reducing activation currents of the 10 first and second differential circuits based on a switch control signal, and positive feedback loops of the first and second differential circuits. A switch part is disposed in each of the positive feedback loops of the first and second differential 15 circuits, disconnecting the positive feedback loops in response to the disconnection timing signal. bias part stops the operation of the first and second differential circuits by reducing the activation currents of the first and second 20 differential circuits by reduction of the bias currents.